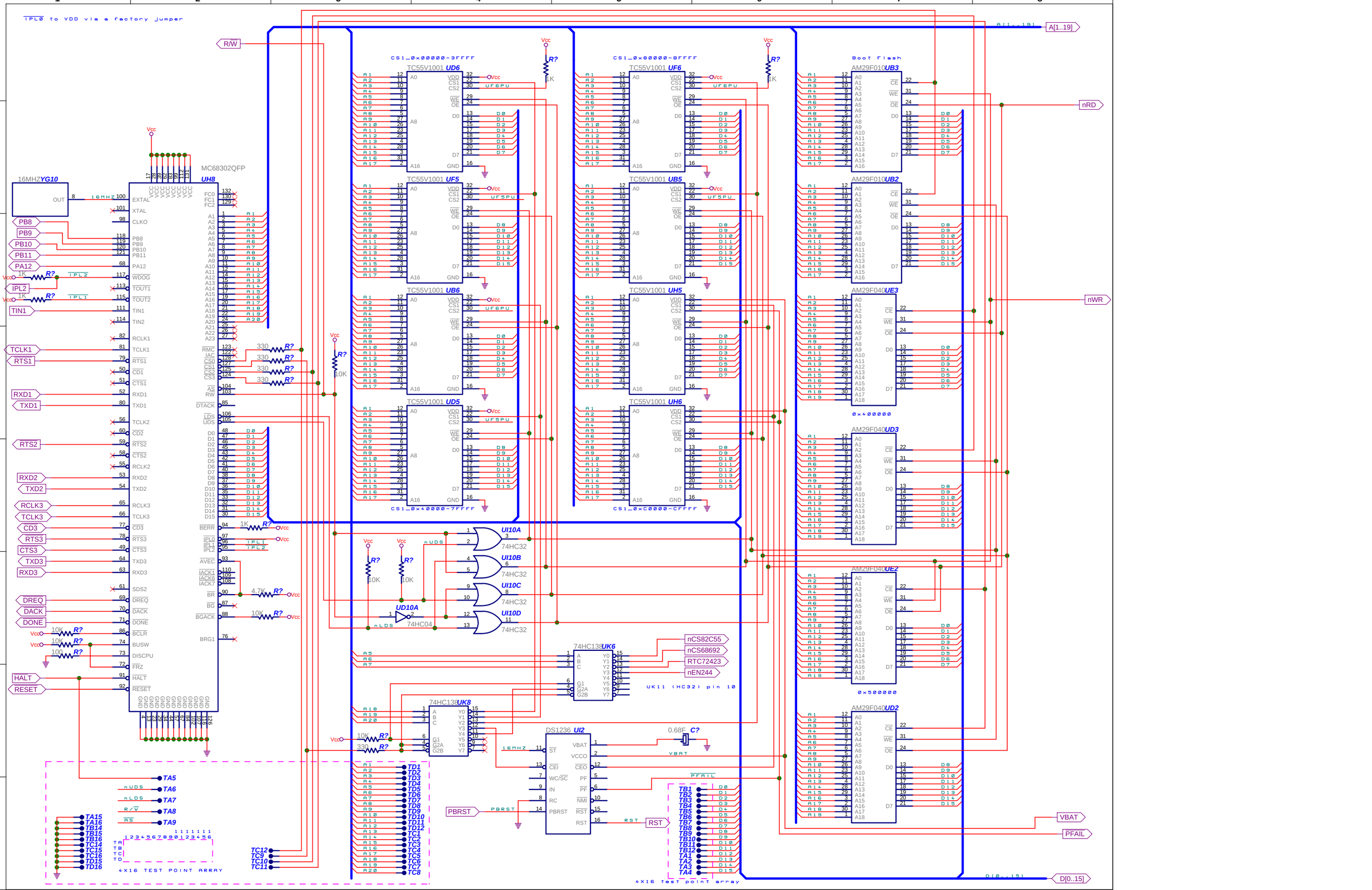
TPL0 to VDD via a factory jumper
R/W
A[1..19]
MC68302QFP
UH8
16MHZ
YG10
CS1_0x000000-3FFFF
CS1_0x400000-7FFFF
CS1_0x800000-BFFFF
CS1_0xC00000-CFFFF
TC55V1001 UD6
TC55V1001 UF6
TC55V1001 UF5
TC55V1001 UB5
TC55V1001 UB6
TC55V1001 UH5
TC55V1001 UD5
TC55V1001 UH6
Boot Flash
AM29F010 UB3
AM29F010 UB2
AM29F040 UE3
AM29F040 UD3
AM29F040 UE2
AM29F040 UD2
0x400000
0x500000
74HC32 UI10A
74HC32 UI10B
74HC32 UI10C
74HC32 UI10D
74HC04 UD10A
74HC138 UK6
74HC138 UK8
UK11 (HC32) pin 10
DS1236 UI2
nCS82C55
nCS68692
RTC72423
nEN244
nRD
nWR
VBAT
PFAIL
RST
PBRST
4X16 TEST POINT ARRAY
4X16 test point array
D[0..15]
PB8
PB9
PB10
PB11
PA12
IPL2
TIN1
TCLK1
RTS1
RXD1
TXD1
RTS2
RXD2
TXD2
RCLK3
TCLK3
CD3
RTS3
CTS3
TXD3
RXD3
DREQ
DACK
DONE
HALT
RESET
BUSW
DISCPU
BCLR
FRZ
WDOG
TOUT1
TOUT2
TIN2
RCLK1
CD1
CTS1
CD2
CTS2
RCLK2
SDS2
BERR
AVEC
BR
BG
BGACK
BRG1
DTACK
AS
RMC
IAC
CS0
CS1
CS2
CS3
LDS
UDS
IACK1
IACK6
IACK7
IPL0
IPL1
IPL2
TA5
TA6
TA7
TA8
TA9
TA15
TA16
TB14
TB15
TB16
TC14
TC15
TC16
TD15
TD16
TD1
TD2
TD3
TD4
TD5
TD6
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