

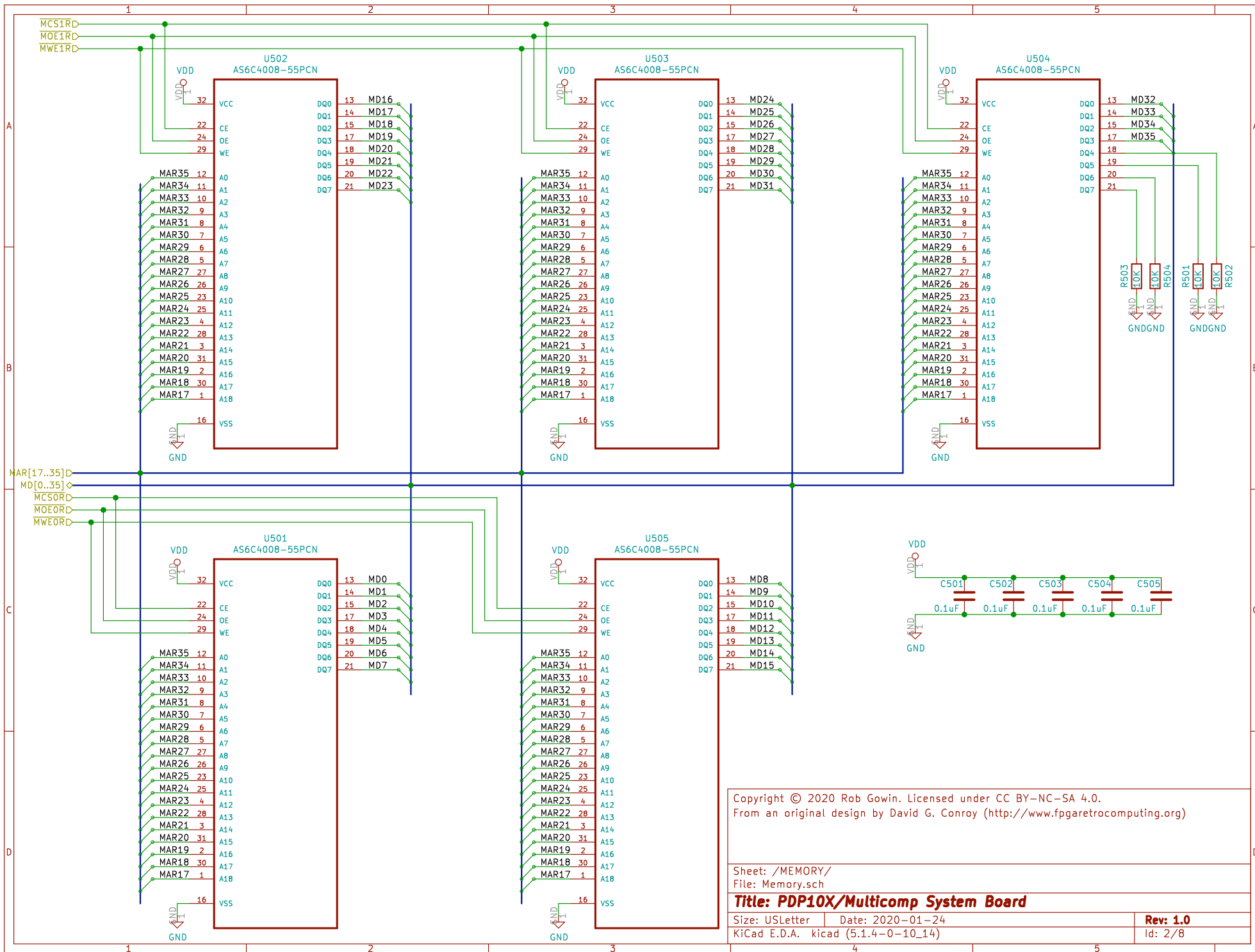
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 From an original design by David G. Conroy (<http://www.fpgaretrocomputing.org>)

Sheet: /
 File: PDP10X.sch

Title: PDP10X/Multicomp System Board

Size: USLetter | Date: 2020-01-24
 KiCad E.D.A. kicad (5.1.4-0-10_14)

Rev: 1.0
 Id: 1/8



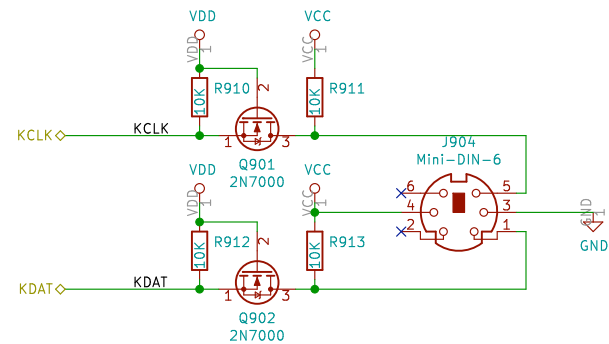
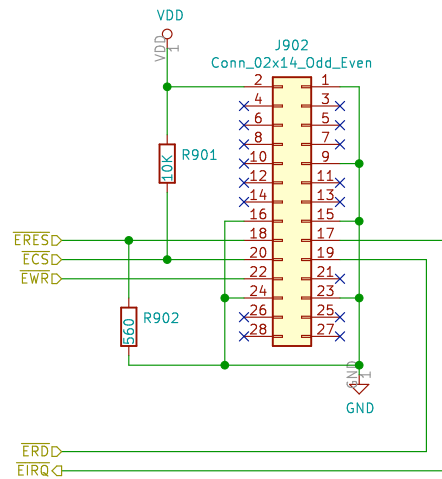
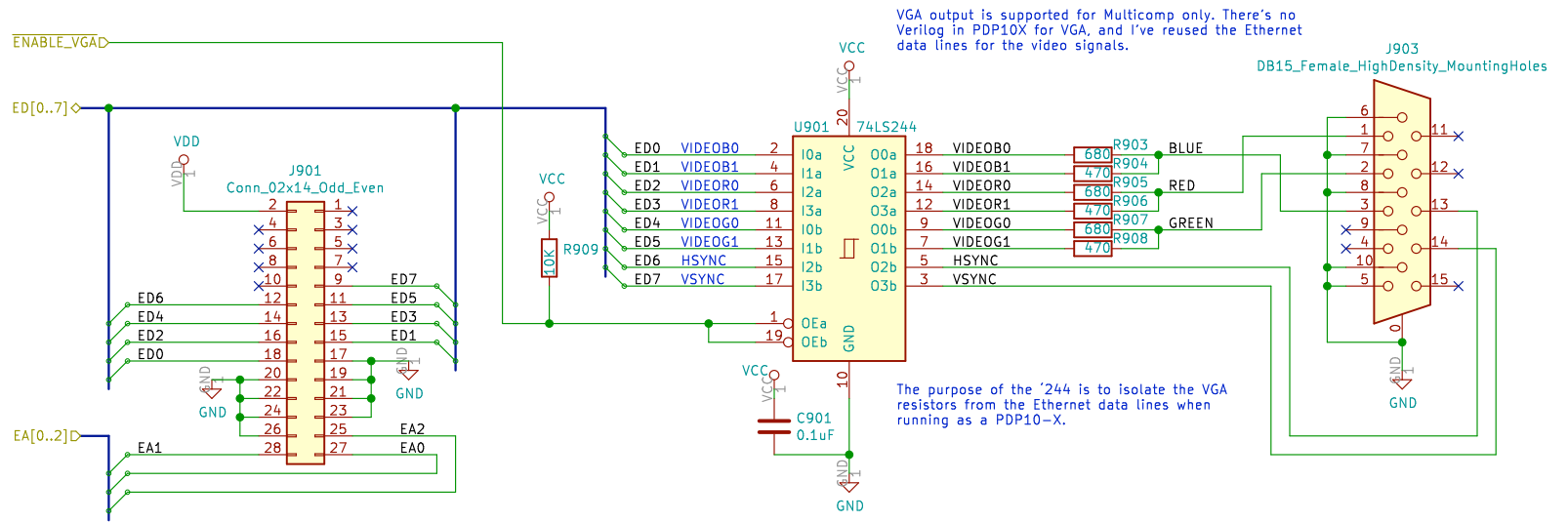
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Sheet: /MEMORY/
 File: Memory.sch

Title: PDP10X/Multicomp System Board

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PS/2 keyboard interface is also only supported for Multicomp.

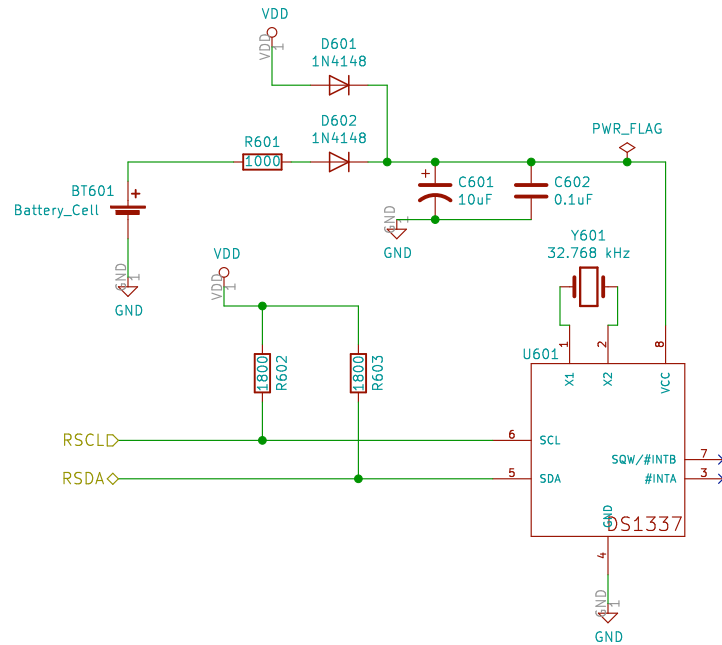
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Sheet: /ETHERNET/VGA/KBRD/
 File: Ethernet.sch

Title: PDP10X/Multicomp System Board

Size: USLetter | Date: 2020-01-24
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Rev: 1.0
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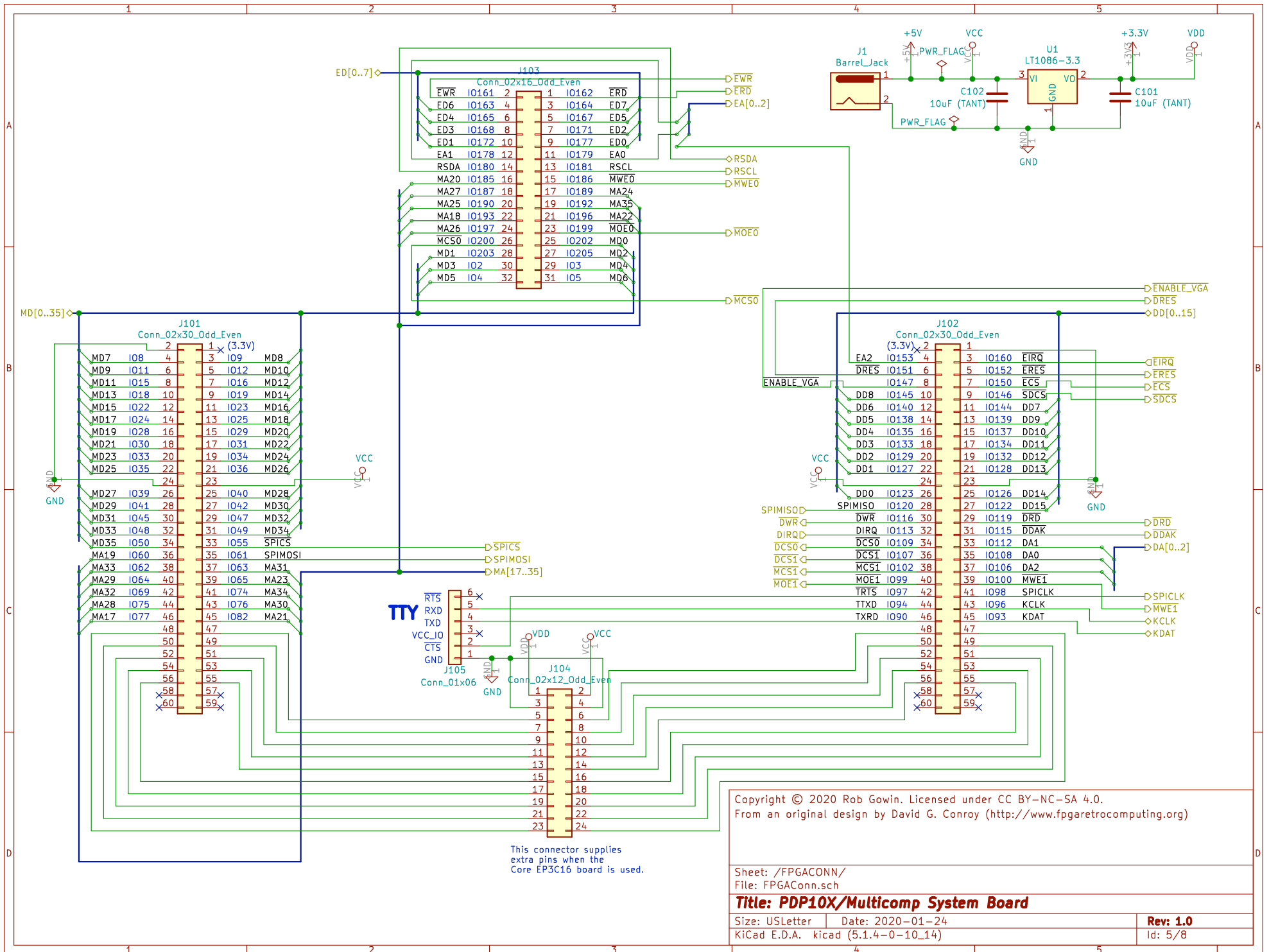
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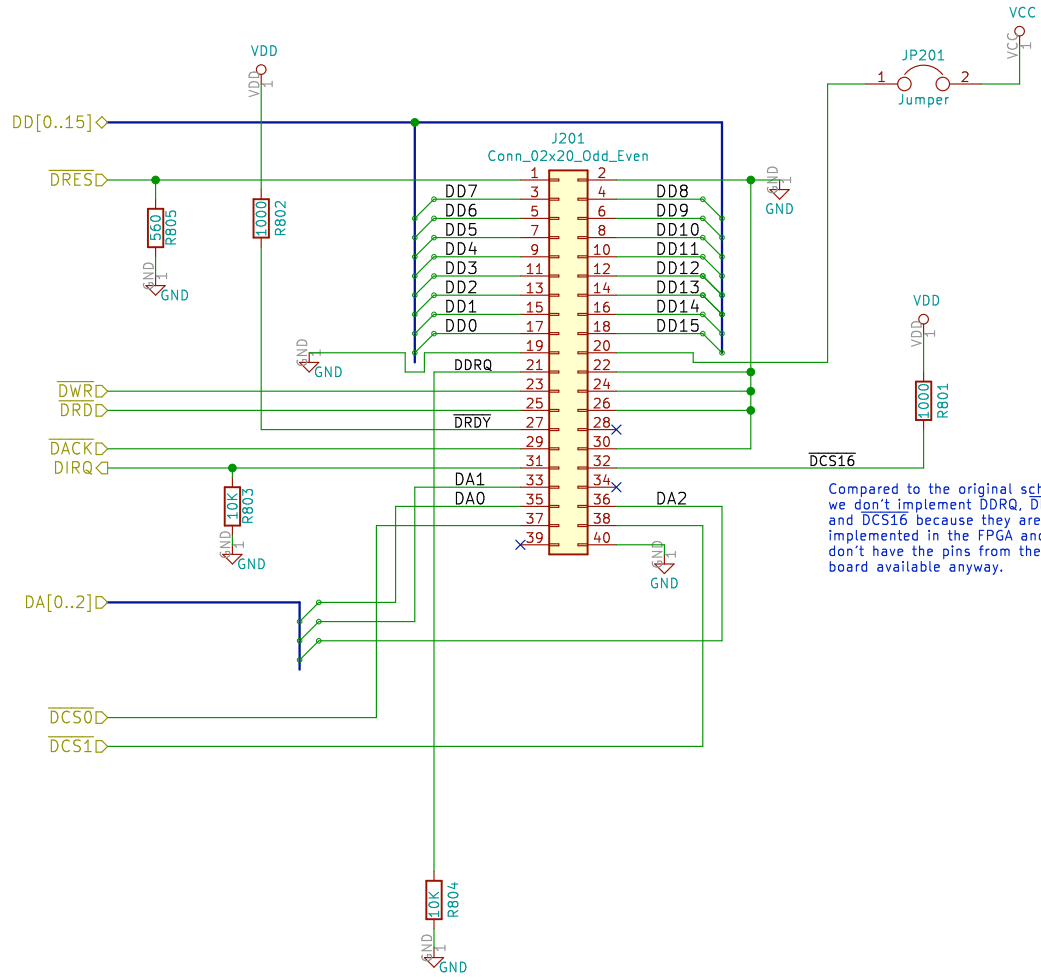
Sheet: /RTC/
 File: RTC.sch

Title: PDP10X/Multicomp System Board

Size: USLetter Date: 2020-01-24
 KiCad E.D.A. kicad (5.1.4-0-10_14)

Rev: 1.0
 Id: 4/8

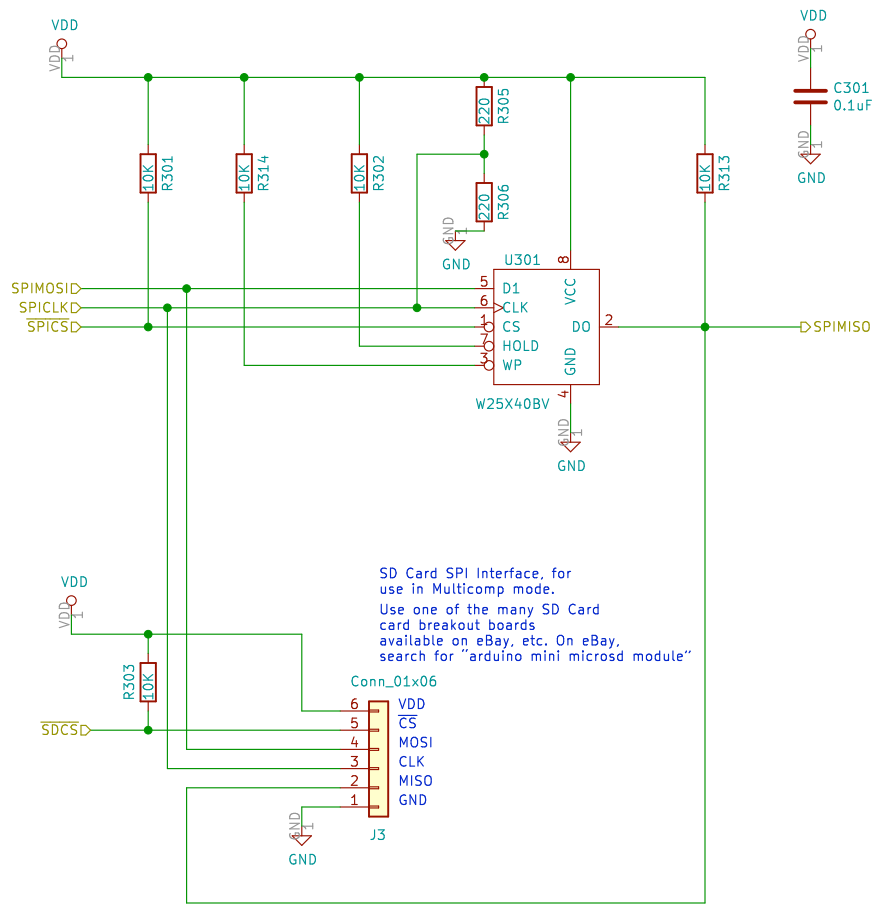




Compared to the original schematic, we don't implement DDRQ, DRDY and DCS16 because they aren't implemented in the FPGA and we don't have the pins from the FPGA board available anyway.

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Sheet: /IDE/ File: IDE.sch	
Title: PDP10X/Multicomp System Board	
Size: USLetter	Date: 2020-01-24
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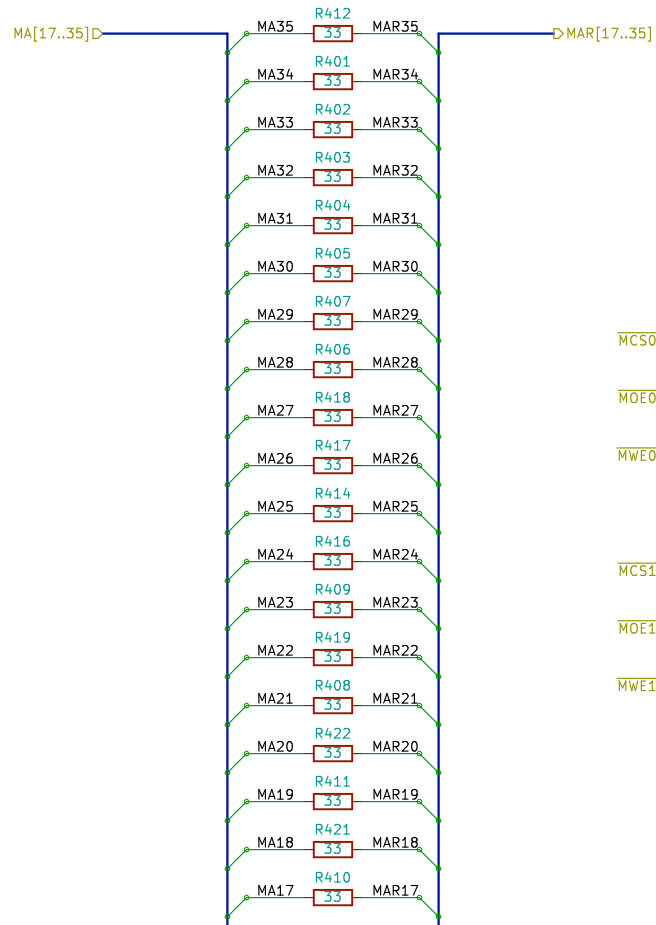
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Sheet: /PROM/SDCARD/
File: PROM.sch

Title: PDP10X/Multicomp System Board

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Sheet: /SERIESTERM/
 File: SERIESTERM.sch

Title: PDP10X/Multicomp System Board

Size: USLetter Date: 2020-01-24
 KiCad E.D.A. kicad (5.1.4-0-10_14)

Rev: 1.0
 Id: 8/8