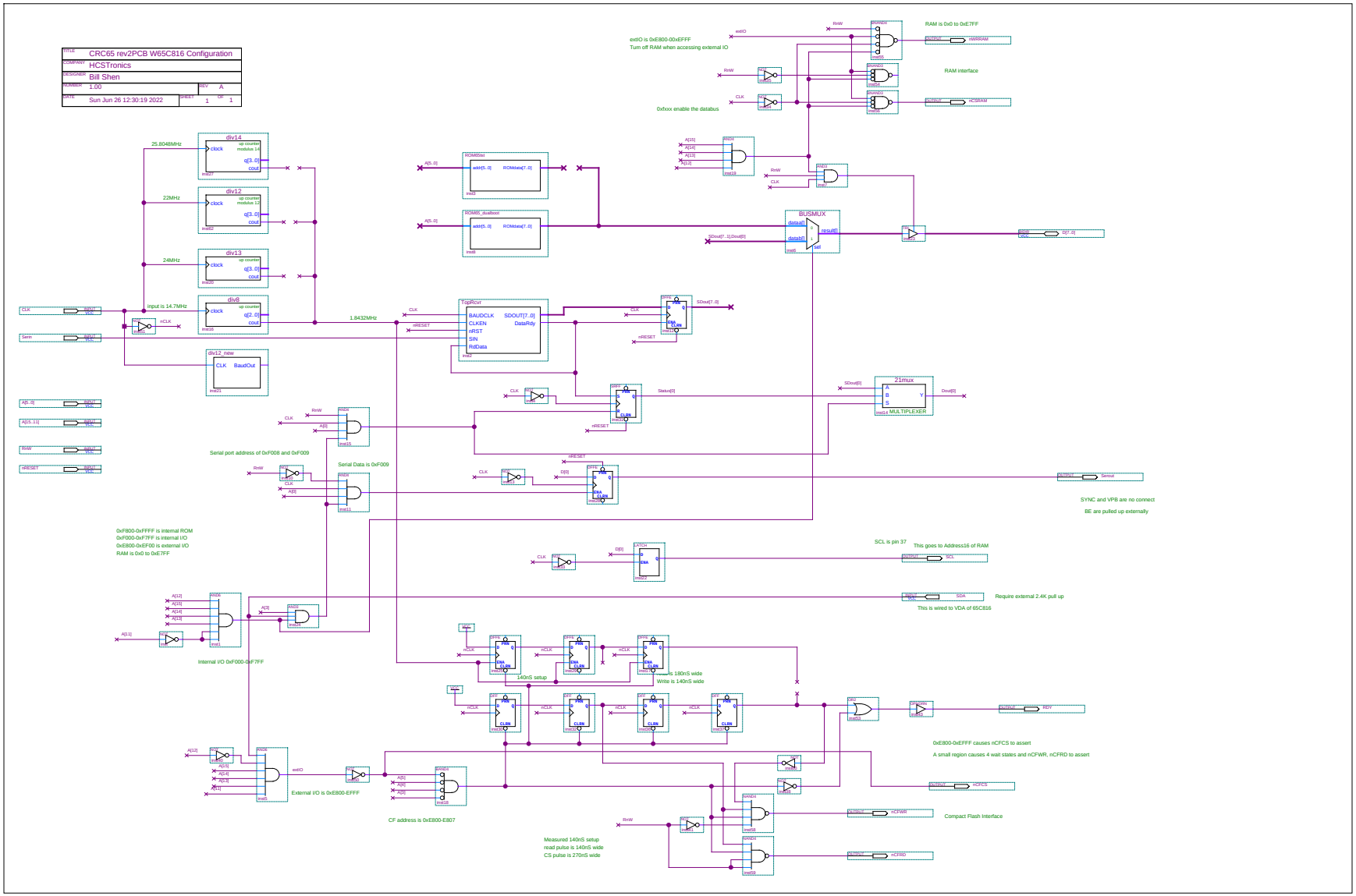
TITLE CRC65 rev2PCB W65C816 Configuration
COMPANY HCSTronics
DESIGNER Bill Shen
NUMBER 1.00
REV A
DATE Sun Jun 26 12:30:19 2022
SHEET 1 OF 1
extIO is 0xE800-00xEFFF
Turn off RAM when accessing external IO
RAM is 0x0 to 0xE7FF
RAM interface
0xfxxx enable the databus
div14
div12
div13
div8
div12_new
25.8048MHz
22MHz
24MHz
Input is 14.7MHz
1.8432MHz
TopRcvr
BAUDCLK
CLKEN
nRST
SIN
RxData
SDOUT[7..0]
DataRdy
BUSMUX
21mux
MULTIPLEXER
Serial port address of 0xF008 and 0xF009
Serial Data is 0xF009
0xF800-0xFFFF is internal ROM
0xF000-0xF7FF is internal I/O
0xE800-0xEF00 is external I/O
RAM is 0x0 to 0xE7FF
SYNC and VPB are no connect
BE are pulled up externally
SCL is pin 37
This goes to Address16 of RAM
Require external 2.4K pull up
This is wired to VDA of 65C816
Internal I/O 0xF000-0xF7FF
140nS setup
Write is 180nS wide
0xE800-0xEFFF causes nCFCS to assert
A small region causes 4 wait states and nCFWR, nCFRD to assert
External I/O is 0xE800-EFFF
CF address is 0xE800-E807
Compact Flash Interface
Measured 140nS setup
read pulse is 140nS wide
CS pulse is 270nS wide