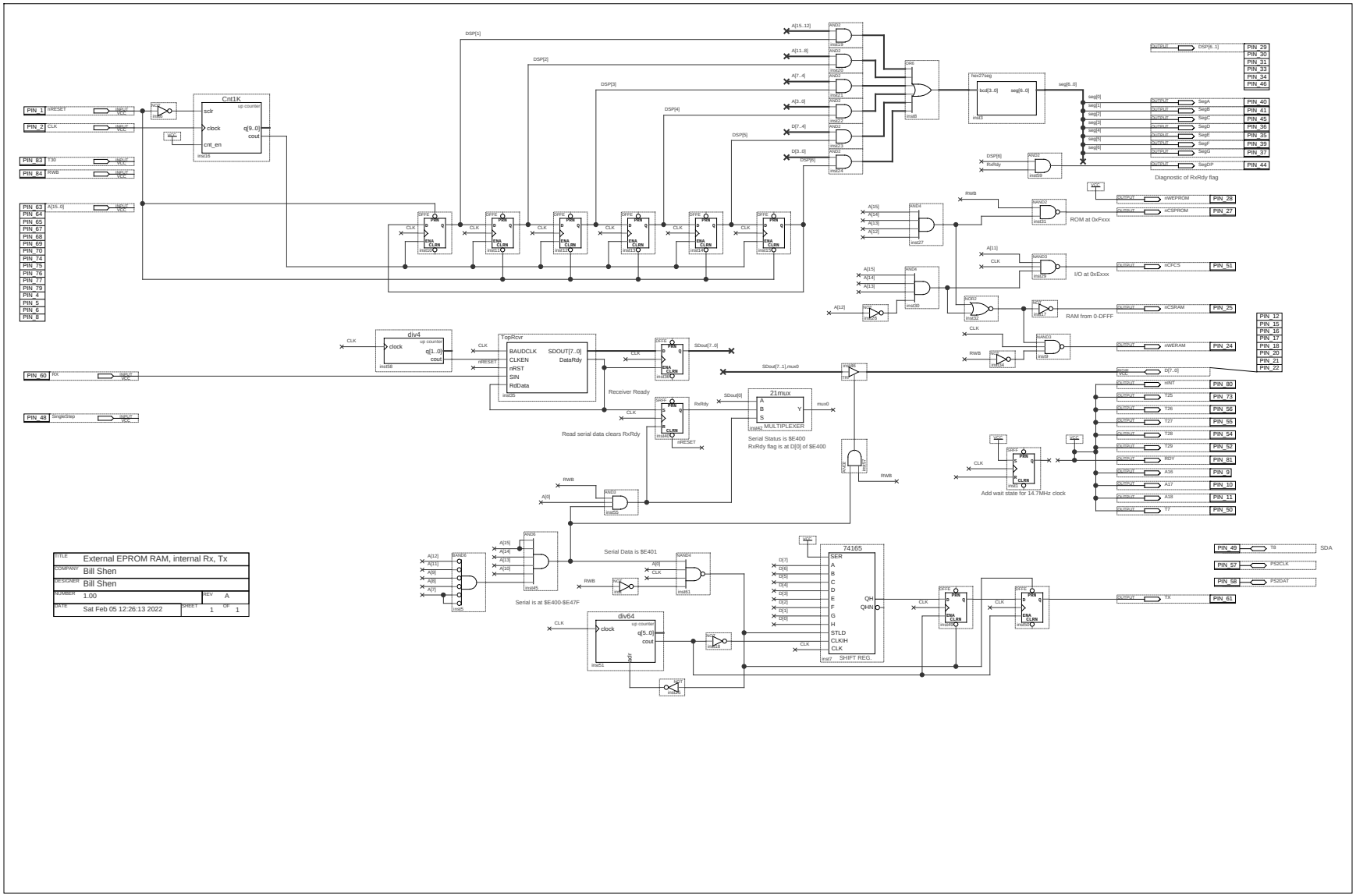
Cnt1K
div4
TopRcvr
BAUDCLK
CLKEN
nRST
SIN
RdData
SDOUT[7..0]
DataRdy
Receiver Ready
Read serial data clears RxRdy
21mux
MULTIPLEXER
Serial Status is $E400
RxRdy flag is at D[0] of $E400
Serial Data is $E401
Serial is at $E400-$E47F
div64
74165
SHIFT REG.
hex27seg
Diagnostic of RxRdy flag
ROM at 0xFxxx
I/O at 0xExxx
RAM from 0-DFFF
Add wait state for 14.7MHz clock
PIN_1 nRESET
PIN_2 CLK
PIN_83 *30
PIN_84 RWB
PIN_63 A[15..0]
PIN_60 RX
PIN_48 SingleStep
PIN_29 DSP[6..1]
PIN_40 SegA
PIN_41 SegB
PIN_45 SegC
PIN_36 SegD
PIN_35 SegE
PIN_39 SegF
PIN_37 SegG
PIN_44 SegDP
PIN_28 nWEPROM
PIN_27 nCSPROM
PIN_51 nCFCS
PIN_25 nCSRAM
PIN_24 nWERAM
PIN_12 D[7..0]
PIN_80 nINT
PIN_73 T25
PIN_56 T26
PIN_55 T27
PIN_54 T28
PIN_52 T29
PIN_81 RDY
PIN_9 A16
PIN_10 A17
PIN_11 A18
PIN_50 T7
PIN_49 T8 SDA
PIN_57 PS2CLK
PIN_58 PS2DAT
PIN_61 TX
TITLE External EPROM RAM, internal Rx, Tx
COMPANY Bill Shen
DESIGNER Bill Shen
NUMBER 1.00
REV A
DATE Sat Feb 05 12:26:13 2022
SHEET 1 OF 1