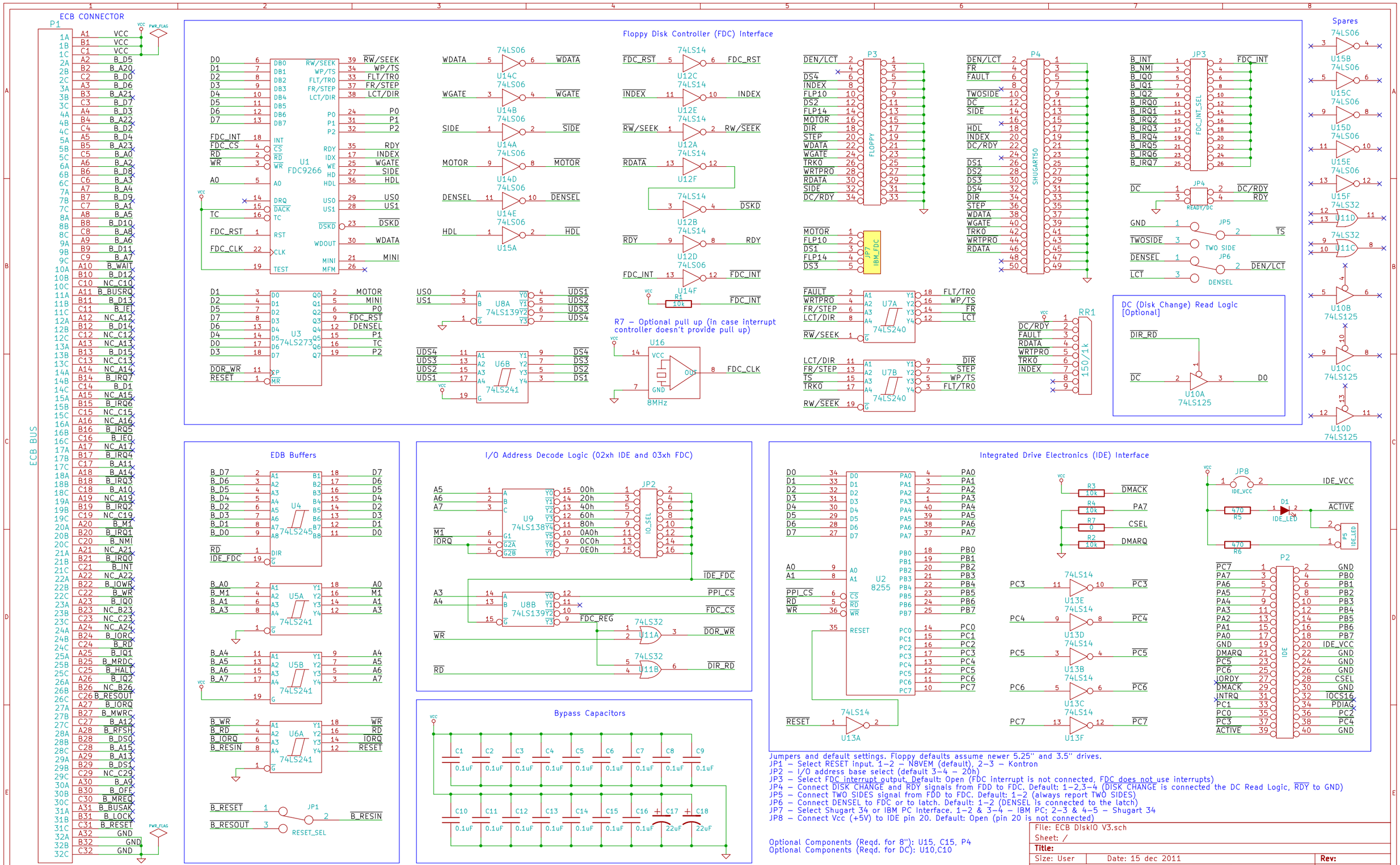
ECB CONNECTOR
P1
ECB BUS
Floppy Disk Controller (FDC) Interface
U1 FDC9266
U3 74LS273
U8A 74LS139
U6B 74LS241
U16
8MHz
R7 – Optional pull up (in case interrupt controller doesn't provide pull up)
P3 FLOPPY
P4 SHUGART50
JP7 IBM_FDC
U7A 74LS240
U7B 74LS240
RR1 150/1k
JP3 FDC_INT_SEL
JP4 READY/DC
JP5 TWO SIDE
JP6 DENSEL
DC (Disk Change) Read Logic [Optional]
U10A 74LS125
Spares
U15B 74LS06
U15C 74LS06
U15D 74LS06
U15E 74LS06
U15F 74LS06
U11D 74LS32
U11C 74LS32
U10B 74LS125
U10C 74LS125
U10D 74LS125
EDB Buffers
U4 74LS245
U5A 74LS241
U5B 74LS241
U6A 74LS241
JP1 RESET_SEL
I/O Address Decode Logic (02xh IDE and 03xh FDC)
U9 74LS138
U8B 74LS139
JP2 IO_SEL
00h
20h
40h
60h
80h
0A0h
0C0h
0E0h
U11A 74LS32
U11B 74LS32
Bypass Capacitors
Integrated Drive Electronics (IDE) Interface
U2 8255
U13A 74LS14
U13E 74LS14
U13D 74LS14
U13B 74LS14
U13C 74LS14
U13F 74LS14
JP8 IDE_VCC
P5 IDE_LED
P2 IDE
Jumpers and default settings. Floppy defaults assume newer 5.25" and 3.5" drives.
JP1 – Select RESET input. 1–2 – N8VEM (default), 2–3 – Kontron
JP2 – I/O address base select (default 3–4 – 20h)
JP3 – Select FDC interrupt output. Default: Open (FDC interrupt is not connected, FDC does not use interrupts)
JP4 – Connect DISK CHANGE and RDY signals from FDD to FDC. Default: 1–2,3–4 (DISK CHANGE is connected the DC Read Logic, RDY to GND)
JP5 – Connect TWO SIDES signal from FDD to FDC. Default: 1–2 (always report TWO SIDES)
JP6 – Connect DENSEL to FDC or to latch. Default: 1–2 (DENSEL is connected to the latch)
JP7 – Select Shugart 34 or IBM PC Interface. 1–2 & 3–4 – IBM PC; 2–3 & 4–5 – Shugart 34
JP8 – Connect Vcc (+5V) to IDE pin 20. Default: Open (pin 20 is not connected)
Optional Components (Reqd. for 8"): U15, C15, P4
Optional Components (Reqd. for DC): U10,C10
File: ECB DiskIO V3.sch
Sheet: /
Title:
Size: User
Date: 15 dec 2011
Rev:
KiCad E.D.A. eeschema (2011-03-23 BZR 2918)-stable
Id: 1/1