

1					2					3					4					5				
Sheet: CPU_80C188					Sheet: ECBBus					Sheet: BUSdrive					Sheet: IDE_1_2					Sheet: IDE_1_2				
2					3					6					8					8				
File: CPU_80C188.sch					File: ECBBus.sch					File: BUSdrive.sch					File: IDE_1_2.sch					File: IDE_1_2.sch				
Sheet: Memory					Sheet: Floppy					Sheet: UART_RTC					Sheet: IDE_1_2					Sheet: IDE_1_2				
4					5					7					8					8				
File: Memory.sch					File: Floppy.sch					File: UART_RTC.sch					File: IDE_1_2.sch					File: IDE_1_2.sch				













