Subject: ZSID patched for Z180
Posted by zenxyzzy on Mon, 21 Feb 2022 05:02:52 GMT
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ZSID, for which I have no DRI sources, chokes horribly on TSTIO,
TST, IN0, OUT0, etc.   this means that I can't single step Z180
specific code, and have to use Z80 equivalent subroutines for all
the instructions i really want to use for those tight inner loops.

has anybody ever built ZSID from source? I'm competent to add the
instructions, but I'm given to understand that ZSID, like DDT, has
special code to relocate itself to high memory using a PRL-like
mechanism. how in the world do you build that?

 https://mark-ogden.uk/mirrors/www.cirsovius.de/CPM/Projekte/ Disassembler/ZSID-en.html

has a hunk of disassembled ZSID source, but I have no idea how to interpret
his instructions for generating a runnable binary.

I'm hoping somebody has already patched ZSID for Z180, so I don't
need to reinvent the wheel.

--curt

Subject: Re: ZSID patched for Z180
Posted by zenxyzzy on Tue, 22 Feb 2022 08:49:59 GMT
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I've managed to make ZSID build from Cirsovius's source after a day of work.
now adding the Z180 stuff.

--curt

Subject: Re: ZSID patched for Z180
Posted by hperaza on Mon, 30 May 2022 09:12:11 GMT
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IIRC, C.B. Falconer's DDTZ supported Z180 opcodes, but it used Intel-style mnemonics for the
disassembly display and the embedded assembler. Z8E (my favorite) uses Zilog mnemonics and
can be assembled for either Z80 or Z180, but being a full-screen debugger it requires proper
terminal support/setup.