
Subject: N8 (4314) RomWBW 3.0.1 and PPIDE
Posted by [andrii_kutepov](#) on Sun, 31 Jan 2021 10:45:39 GMT
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Hi,

Can't make PPIDE work on N8 (4314).

I add to my config file

```
PPIDEENABLE .SET TRUE ; PPIDE: ENABLE PARALLEL PORT IDE DISK DRIVER  
(PPIDE.ASM)  
PPIDE0A8BIT .SET TRUE ; PPIDE 0A (MASTER): 8 BIT XFER  
PPIDE0B8BIT .SET TRUE ; PPIDE 0B (SLAVE): 8 BIT XFER  
PPIDE0BASE .SET $80 ; PPIDE 1: PPI REGISTERS BASE ADR
```

I'm use some previous tested CF's with CF-IDE adapter, different tested ppi 8255. Double check all signal and pcb track.

also added wait states on IO and memory.

Without a result. The card is not determined at the start. Sometimes with different errors.

The question arose when I didn't find a file .\Doc\Testing Notes.txt
mention of the testing of the ppide. there's only this:

N8-2312 (X)

- Test ASCI driver
- Test SD driver (CSIO mode)
- Test FD driver
- Test FDU app
- Test TMS driver (video & kbd)

Please say should PPIDE work on N8 with this BIOS ?

Thanks
Andrii

Subject: Re: N8 (4314) RomWBW 3.0.1 and PPIDE
Posted by [b1ackmai1er](#) on Mon, 01 Feb 2021 08:37:46 GMT
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Hi Andrii,

I dont have an N8 but your config changes look good to me. PPIDE on N8 should work, basically identical to SBC-V2

Have you removed Jumpers JP8 and JP7?

Have you checked power requirements for CF-IDE adaper i.e. on N8 K2 jumper and any jumpering on CF-IDE board itself.

What error messages are you getting?

Do you have the Front panel IDE LED hooked up? Is there any activity on it.

Have you checked orientation of pin 1 of cf-ide card versus pin 1 on N8? If so get up turn around 3 times anticlockwise, then 2 times clockwise, then sit down, chant for 30 seconds then check again.

You could try writing a simple BASIC program to test the 8255 i.e. flash the IDE led, toggle some lines [https://www.geeksforgeeks.org/programmable-peripheral-interf ace-8255/](https://www.geeksforgeeks.org/programmable-peripheral-interf-ace-8255/)

Best of luck Phil.

Subject: Re: N8 (4314) RomWBW 3.0.1 and PPIDE
Posted by [andrii_kutepov](#) on Mon, 01 Feb 2021 20:32:29 GMT
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Hi Phil,

Thank you for you answer.

Of course i checked 1pin adapter. and jumper K2 for power (pin20). This adaper with this CF perfect worked in Z180 Mark IV. i8255 also tested to other hardware. JP7 and JP8 no seted. Error most often - NO MEDIA. sometimes - DRQ TIMEOUT or READY TIMEOUT.

I think maybe there's a problem with the BIOS I've compiled. Maybe Wayne could have commented.

BR
Andrii

Subject: Re: N8 (4314) RomWBW 3.0.1 and PPIDE
Posted by [andrii_kutepov](#) on Sat, 06 Feb 2021 07:50:54 GMT
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I brainstormed. And rang all the tracks around the PPI and IDE connector. I found output a PPI panel with cold solder. This is the pin of 12 - PC5. My stupid mistake cost me many hours of tests. I'm sorry to bother you. Now everything works perfectly.

RomWBW HBIOS v3.1.1-pre.40, 2021-02-06

N8 Z8S180-N @ 18.432MHz IO=0x40

0 MEM W/S, 2 I/O W/S, INT MODE 2, MMU=N8
512KB ROM, 1024KB RAM

AY: IO=0x9C
ASCI0: IO=0x40 ASCI W/BRG MODE=38400,8,N,1
ASCI1: IO=0x41 ASCI W/BRG MODE=38400,8,N,1
DSRTC: MODE=STD IO=0x88 Sat 2021-02-06 09:50:31 CHARGE=ON
TMS: IO=0x98
PPK: IO=0x84
MD: UNITS=2 ROMDISK=384KB RAMDISK=768KB
FD: IO=0x8C UNITS=2
PPIDE: IO=0x80
PPIDE0: LBA BLOCKS=0x003D8000 SIZE=1968MB
PPIDE1: NO MEDIA
SD: MODE=CSIO OPR=0x88 CNTR=0x4A TRDR=0x4B DEVICES=1
SD0: SDSC NAME=SU128 BLOCKS=0x0003C100 SIZE=120MB

*** Activating CRT Console ***

Thank you all very much for your help.

Good luck.
Andrii
