
Subject: Re: CB030, A 68030 SBC for hobbyists
Posted by [mikesmith](#) on Sat, 08 Feb 2020 07:39:46 GMT
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One more memory map question:

CFdata equ \$FFFFE000 * CF data register
CFerr equ \$FFFFE001 * CF error reg

is the plan to stick with an 8-bit CF interface (I'm guessing pin restrictions again?)