
Subject: Re: CB030, A 68030 SBC for hobbyists
Posted by [plasmo](#) on Fri, 07 Feb 2020 03:14:25 GMT
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The swap register is a hack. I didn't want to deal with MMU translation initially, so I use swap register to exchange first 64meg memory with 2nd 64 meg memory. The first 64 meg is EPROM and DRAM starts from 2nd 64 meg. So if I do have 128meg of DRAM, then the memory map after swap is

lower half DRAM at 1st 64meg (0-3FFFFFFF)

EPROM at 2nd 64 meg (4000000-7FFFFFFF)

upper half DRAM at 3rd 64 meg (8000000-BFFFFFFF)

The reason I don't just swap the first 128meg with 2nd 128 meg is because I ran out of pins so the total decoded memory space is 256meg and the top 64 meg is I/O space. I don't want to change I/O mapping when swapping.

Swap register probably won't be in use once MMU translation is turned on.

Bill
