mikemac wrote on Sat, 25 March 2017 23:05OK the NS32202 must right the interrupt level on the data bus during the ack cycle since only B_/INT and B_/NMI are the only interrupt signals used by the mini-m68k and kiss68030 boards.

I still can't find a periodic timer in the Jackalope design. I hope I'm just missing it because if it's not there, that would be "a bad thing", IMNSHO.

Thanks for giving me the hint as how the mini-m68k and kiss68030 work via the mfc-pic!

Look at the 68230 - it has programmable timers in it so Jackalope has a periodic timer.