
Subject: Re: Gryphon 68030

Posted by [mikemac](#) on Sun, 26 Mar 2017 04:05:43 GMT

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OK the NS32202 must right the interrupt level on the data bus during the ack cycle since only B_/INT and B_/NMI are the only interrupt signals used by the mini-m68k and kiss68030 boards.

I still can't find a periodic timer in the Jackalope design. I hope I'm just missing it because if it's not there, that would be "a bad thing", IMNSHO.

Thanks for giving me the hint as how the mini-m68k and kiss68030 work via the mfc-pic!
