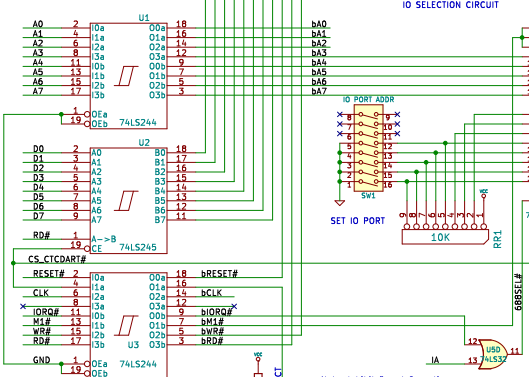
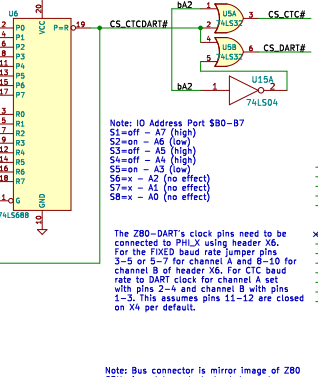


Z80 BUS INTERFACE



Note: Buffers and Transceivers respond to IO and MEM cycles

IO SELECTION CIRCUIT



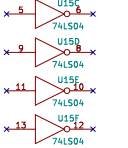
Note: Inhibit Board Operation During Interrupts

Note: ID Address Port \$80-\$B7
S1=off - A7 (high)
S2=on - A6 (low)
S3=off - A5 (high)
S4=off - A4 (high)
S5=on - A3 (low)
S6=x - A2 (no effect)
S7=x - A1 (no effect)
S8=x - A0 (no effect)

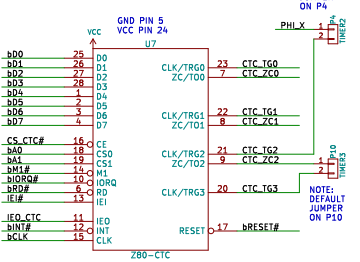
Note: Bus connector is mirror image of Z80 CPU pin out to mate to backplane whose connectors reflect Z80 CPU pin out.

Table of bus connector pin assignments for A0 to SPARE10.

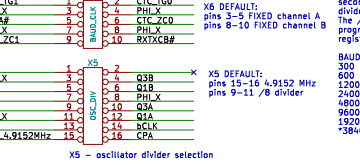
Spare Components



Z80-DART



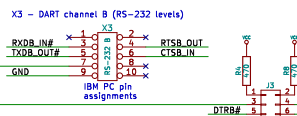
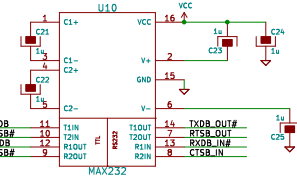
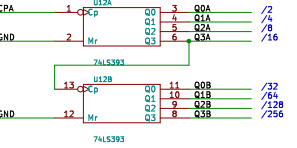
X6 - DART baud rate clock source selection
X5 - oscillator divider selection



First jumper pins 15-16 of X5 to select the oscillator module. A second jumper on X5 selects the divider output for PH1X.

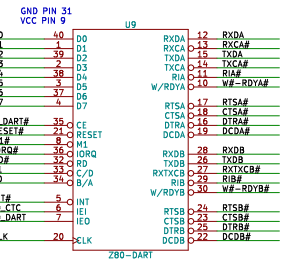
BAUD PH1X (Hz) /16 or /64
300 19200 /64
600 38400 /64
1200 19200 /16
2400 38400 /16
4800 76800 /16
9600 153600 /16
19200 307200 /16
38400 614400 /16

NOTE: JUMPER DEFAULTS FOR 38400 bps with 50 Hz System Tick
- Assume 4.9152 MHz oscillator
- Jumper X5 pins 15-16 and pins 9-11 (/8) to X4 pin 7 (CIC-TG3)
- Jumper X5 pin 8 (CIC-ZC2) to X4 pin 9 (CIC-TG3)



NOTE: J3 CONFIGURES DTR FOR RS-232A AND B CHANNELS. DEFAULT IS 1-3 AND 2-4. TTL DTR AVAILABLE FOR CUSTOM APPLICATIONS.

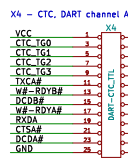
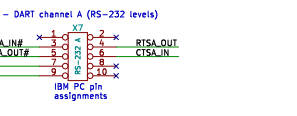
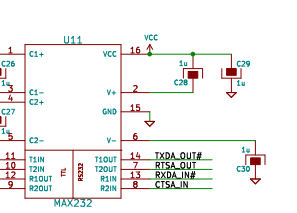
Z80-DART



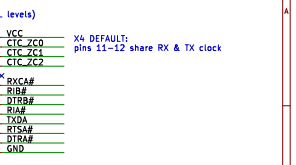
U9 - DART channel A (TTL levels)

PH1X, PH1A, PH1B, PH1C, PH1D, PH1E, PH1F, PH1G, PH1H, PH1I, PH1J, PH1K, PH1L, PH1M, PH1N, PH1O, PH1P, PH1Q, PH1R, PH1S, PH1T, PH1U, PH1V, PH1W, PH1X, PH1Y, PH1Z

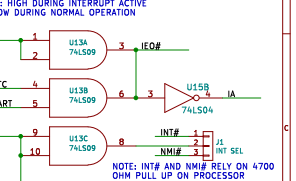
NOTE: JUMPER DEFAULTS FOR 38400 bps with 50 Hz System Tick
- Assume 4.9152 MHz oscillator
- Jumper X5 pins 15-16 and pins 9-11 (/8) to X4 pin 7 (CIC-TG3)
- Jumper X5 pin 8 (CIC-ZC2) to X4 pin 9 (CIC-TG3)



NOTE: DEFAULT JUMPER ON PINS 11-12 TO CONNECT TXCA# AND RXCA#



X4 - CTC, DART channel A (TTL levels)
X4 DEFAULT: pins 11-12 share RX & TX clock



NOTE: INT# AND NM# RELY ON 4700 OHM PULL UP ON PROCESSOR

FTDI USB TO TTL SERIAL ADAPTER CABLE

